NexGen
WAFER SYSTEMS

PRESS RELEASE

Singapore, April, 2025

NexGen Wafer Systems Unveils Advancements in Ultra-Precise Wet Etching Powered by Integrated Metrology at SEMICON SEA 2025

NexGen Wafer Systems will present the latest advancements in Integrated Metrology directly on the Tech Stage at SEMICON Southeast Asia 2025 in Singapore.

📍 **Tech Stage Presentation: Thursday, May 22, 14:00**

Integrated Metrology is a core innovation across NexGen Wafer Systems' Wet Etch and Clean platforms. It enables real-time monitoring and control of critical process parameters—such as target substrate thickness, Total Thickness Variation (TTV), and surface roughness—which are essential when thinning substrates like silicon or gallium arsenide. This capability ensures superior process uniformity and yield, particularly in applications where within-wafer, wafer-to-wafer, and lot-to-lot consistency are critical.

Our integrated solutions support:

- Inline measurement and control of substrate thickness
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- End Point Detection (EPD)
- Real-time chemical concentration monitoring –

Advanced Process Control (APC) and detailed analytics

These features ensure optimal process performance, delivering higher reliability, intelligent process feedback, and cost reductions through performance-driven chemical management.

Visitors can also learn more about our Integrated Metrology solutions at Booth #B1633. Our experts will be happy to answer questions and provide live insights.

We're also pleased to host our colleagues from Trymax Semiconductor Equipment at our booth. As part of the Accuron Technologies family, Trymax offers leading plasma-based solutions for ashing, descum, surface preparation, and UV curing.

Come explore the future of process control with NexGen, discover complementary solutions from Trymax, and attend our high-impact talk on Integrated Metrology.

We look forward to seeing you in Singapore!

Your NexGen Wafer Systems Team

About NexGen Wafer Systems

Headquartered in Singapore, NexGen Wafer Systems is a global supplier of wet etch and clean solutions for the semiconductor industry. With engineering and manufacturing facilities in Austria and an international team of over 100 professionals, NexGen supports its customers worldwide in all major semiconductor manufacturing regions.

Through its research and demonstration lab in Singapore, NexGen offers customers a versatile and effective means to showcase and develop wet etch and clean process solutions collaboratively. All within quick development cycles tailored to the industry's fast-paced R&D timelines.

With over 200 process chambers installed globally, NexGen is advancing steadily toward its vision of becoming an international leader in dedicated surface treatment applications.

Further information is available at www.nexgen-wafer-systems.com/sereno

A general press information about NexGen Wafer Systems can be found here

This press release is also available online

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